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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/743,662	12/22/2003	Kenneth Smith	10013804-1	7608	
22879 759 HEWLETT PAC	90 12/28/200 KARD COMPANY	EXAMINER			
P O BOX 272400, 3404 E. HARMONY ROAD			IQBAL, NADEEM		
	CTUAL PROPERTY ADMINISTRATION . LLINS, CO 80527-2400		ART UNIT	PAPER NUMBER	
			2114		
SHORTENED STATUTORY F	PERIOD OF RESPONSE	MAIL DATE	DELIVER	Y MODE	
				DELIVERY MODE	
3 MONTHS		12/28/2006	PAF	'ER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

	Application No.	Applicant(s)
Off' - A - 4' O	10/743,662	SMITH ET AL.
Office Action Summary	Examiner	Art Unit
	Nadeem Iqbal	2114
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 16(a). In no event, however, may a reply be tim rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	l. lely filed the mailing date of this communication. (35 U.S.C. § 133).
Status		
1) Responsive to communication(s) filed on 24 Ja	action is non-final. nce except for formal matters, pro	
Disposition of Claims		
4) Claim(s) is/are pending in the application 4a) Of the above claim(s) is/are withdraw 5) Claim(s) is/are allowed. 6) Claim(s) 1-10 and 18-23 is/are rejected. 7) Claim(s) 11-17,24 is/are objected to. 8) Claim(s) are subject to restriction and/or Application Papers 9) The specification is objected to by the Examiner 10) The drawing(s) filed on is/are: a) access applicant may not request that any objection to the ore Replacement drawing sheet(s) including the corrections.	on from consideration. The election requirement. The epted or b) □ objected to by the Edrawing(s) be held in abeyance. See ton is required if the drawing(s) is objected.	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).
11) The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.
Priority under 35 U.S.C. § 119 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list of the certified copies of the attached detailed Office action for a list of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list of the prior the certified copies of the priority documents are copies of the prior	s have been received. s have been received in Applicati ity documents have been receive (PCT Rule 17.2(a)).	on No ed in this National Stage
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	te

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Response to Amendment

This office action is in response to an amendment filed on Sep 21, 2006.

Double Patenting

1. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. A nonstatutory obviousness-type double patenting rejection is appropriate where the conflicting claims are not identical, but at least one examined application claim is not patentably distinct from the reference claim(s) because the examined application claim is either anticipated by, or would have been obvious over, the reference claim(s). See, e.g., *In re Berg*, 140 F.3d 1428, 46 USPQ2d 1226 (Fed. Cir. 1998); *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and *In re Thorington*, 418 F.2d 528, 163 USPO 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) or 1.321(d) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent either is shown to be commonly owned with this application, or claims an invention made as a result of activities undertaken within the scope of a joint research agreement.

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

2. Claims 1, 2 & 21 are provisionally rejected on the ground of nonstatutory double patenting over claim 17 of copending Application No. 2003/0023928. This is a provisional double patenting rejection since the conflicting claims have not yet been patented.

The subject matter claimed in the instant application is fully disclosed in the referenced copending application and would be covered by any patent granted on that copending application since the referenced copending application and the instant application are claiming common subject matter, as follows: With reference to a magnetic random access memory array that comprises a plurality of magnetic memory cells. The referenced application teaches (claim 17, lines 1-3), With reference to a controller coupled to MRAM array. The referenced application

teaches (claim 17, lines 7-8). With reference to the controller is configured to communicate with a host device and controller is configured to perform an error correction function associated with at least one of the plurality of magnetic memory cells. The referenced application teaches (claim 17, lines 4-6).

Furthermore, there is no apparent reason why applicant would be prevented from presenting claims corresponding to those of the instant application in the other copending application. See *In re Schneller*, 397 F.2d 350, 158 USPQ 210 (CCPA 1968). See also MPEP § 804.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 4. Claims 1-10, 18-23 are rejected under 35 U.S.C. 102(e) as being anticipated by Nahas (U.S. Patent Application number 2005/0144551).
- 5. As per claim 1, Nahas teaches (page 1, para. 0011, lines 1-3) an embedded memory system having error correction circuitry with a counter for counting detected errors. He also teaches an MRAM core, MRAM core also has an error, read and write counts field. He thus teaches a magnetic random access memory (MRAM) array. With reference to a controller

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coupled to the MRAM. He teaches (page 2, para. 0012, lines 4-7). With reference to controller is configured to perform an error correction function. He teaches (page 2, para. 0012, lines 1-4).

- 6. As per claim 2, With reference to the controller comprises an error correction module. He teaches (page 2, para. 0012, lines 1-4).
- 7. As per claims 3 & 4, With reference to the error correction module comprises Reed-Solomon encoding and decoding devices. He teaches (page 1, para. 0012, lines 2-5).
- 8. As per claims 5 & 6, With reference to error correction module is configured to perform a data layout algorithm. He teaches (page 2, para. 0020, lines 1-5).
- 9. As per claims 7 & 8, With reference to the controller is configured to perform the error correction function in response to a write operation. He teaches (page 2, para. 0015, lines 2-6).
- 10. As per claim 9, With reference to the controller is configured to perform the error correction function in response to a read operation. He teaches (page 2, para. 0014, lines 1-6).
- 11. As per claim 10, Nahas teaches (page 1, para. 0011, lines 1-3) an embedded memory system having error correction circuitry with a counter for counting detected errors. He also teaches an MRAM core, MRAM core also has an error, read and write counts field. He thus teaches a magnetic random access memory (MRAM) array. With reference to a controller coupled to the MRAM. He teaches (page 2, para. 0012, lines 4-7). With reference to controller is configured to perform an error correction function. He teaches (page 2, para. 0012, lines 1-4).
- 12. As per claims 18 & 21, Nahas teaches (page 1, para. 0011, lines 1-3) an embedded memory system having error correction circuitry with a counter for counting detected errors. He also teaches an MRAM core, MRAM core also has an error, read and write counts field. He thus teaches a magnetic random access memory (MRAM) array. With reference to a controller

coupled to the MRAM. He teaches (page 2, para. 0012, lines 4-7). With reference to controller is configured to perform an error correction function. He teaches (page 2, para. 0012, lines 1-4).

- 13. As per claims 19 & 22, With reference to the controller is configured to perform the error correction function in response to a write operation. He teaches (page 2, para. 0015, lines 2-6).
- 14. As per claims 20 & 23, With reference to the controller is configured to perform the error correction function in response to a read operation. He teaches (page 2, para. 0014, lines 1-6).

Allowable Subject Matter

15. Claims 11-17, & 24 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Response to Arguments

16. Applicant's arguments with respect to claim 1-24 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nadeem Iqbal whose telephone number is (571)-272-3659. The examiner can normally be reached on M-F (8:00-5:30) First Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Scott Baderman can be reached on (571)-272-3644. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 57\$\begin{array}{c} -272-1000.

Nadeem Iqbal Primary Examiner Art Unit 2114

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